IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Sheueling Chang Shantz et al.
Title:	METHOD AND APPARATUS FOR IMPLEMENTING PROCESSOR INSTRUCTIONS FOR ACCELERATING PUBLIC-KEY CRYPTOGRAPHY

Application No.:	10/626,420	Filed:	July 24, 2003
Examiner:	Not yet assigned	Group Art Unit:	3621

Atty. Docket No.: 004-9388

July 1, 2004

Mail Stop Amendment
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☒   Form(s) PTO-1449 (1 pages), including copy(ies) of 11 reference(s).
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to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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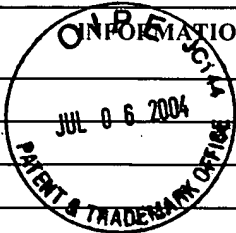☐ facsimile transmitted to the US Patent and Trademark Office.

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Respectfully submitted,

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Application No.: 10/626,420

U.S. Department of Commerce, Patent and Trademark Office		**Attorney Docket No.:**	*004-9388*
		Application No.:	*10/626,420*
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		**Applicant(s):**	*Sheueling Chang Shantz et al.*
(Use several sheets if necessary)		**Filing Date:**	*July 24, 2003*
		Group Art Unit:	*3621*
		Date Submitted:	*July 1, 2004*

NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	AA	Intel® Itanium™ Processor, "High Performance On Security Algorithms (RSA Decryption Kernel)," Intel Corporation, 2001, pp. 1-8.
	AB	Intel®, "Intel® Itanium™ Architecture Software Developer's Manual, Volume 1: Application Architecture," Revision 2.1, October , 2002, 2 pages.
	AC	Großschädl, Johann, "Instruction Set Extension for Long Integer Modulo Arithmetic on RISC-Based Smart Cards," Proceedings of the 14th Symposium on Computer Architecture and High Performance Computing, 2002, 7 pages.
	AD	Koç, Cetin Kaya, "High-Speed RSA Implementation," Version 2.0, RSA Laboratories, November, 1994, pp. i-70.
	AE	Shantz, Sheueling Chang, "From Euclid's GCD to Montgomery Multiplication to the Great Divide," Sun Microsystems, June 2001, pp. 1-10.
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	AG	Woodbury, A.D.; Bailey, Daniel V., Paar, Christof, "Elliptic Curve Cryptography on Smart Cards Without Coprocessors," The Fourth Smart Card Research and Advanced Applications (CARDIS2000) Conference, Bristok, UK, pp. 71-92.
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	AI	D. Bailey and C. Paar, "Optimal Extension Fields for Fast Arithmetic in Public-Key Algorithms." In H. Krawczyk, editor, Advances in Cryptography – CRYPTO '98, volume LNCS 1462, pages 472-485. Springer-Verlag, 1998. http://citeseer.ist.psu.edu/article/bailey98optimal.html, 14 pages.
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Examiner	**Date Considered**

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